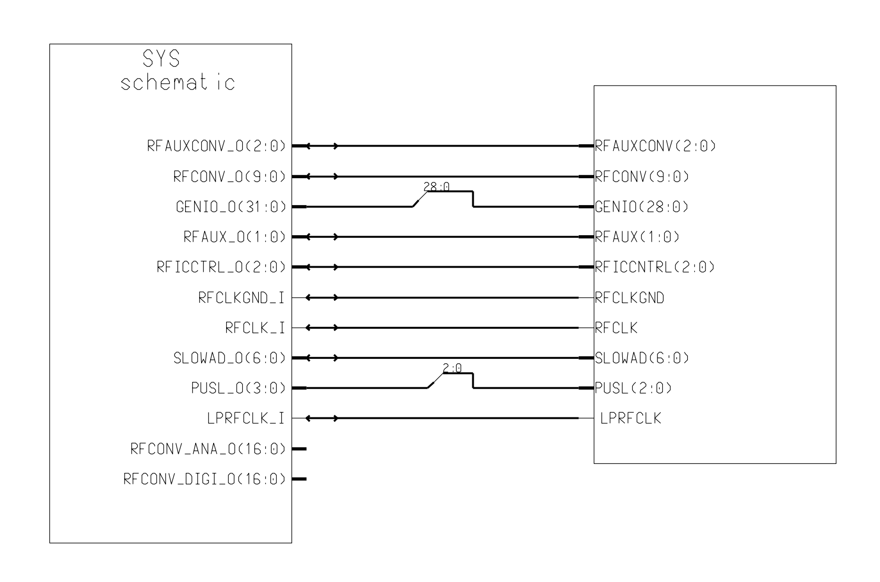
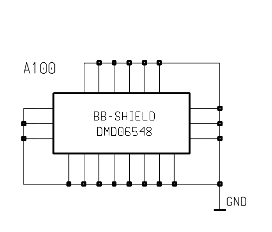
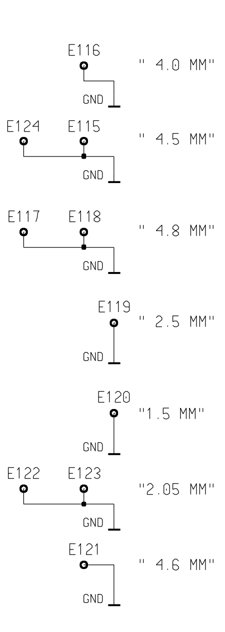
RF-BB Diagram



"GROUND HOLES FOR MECHANICS"

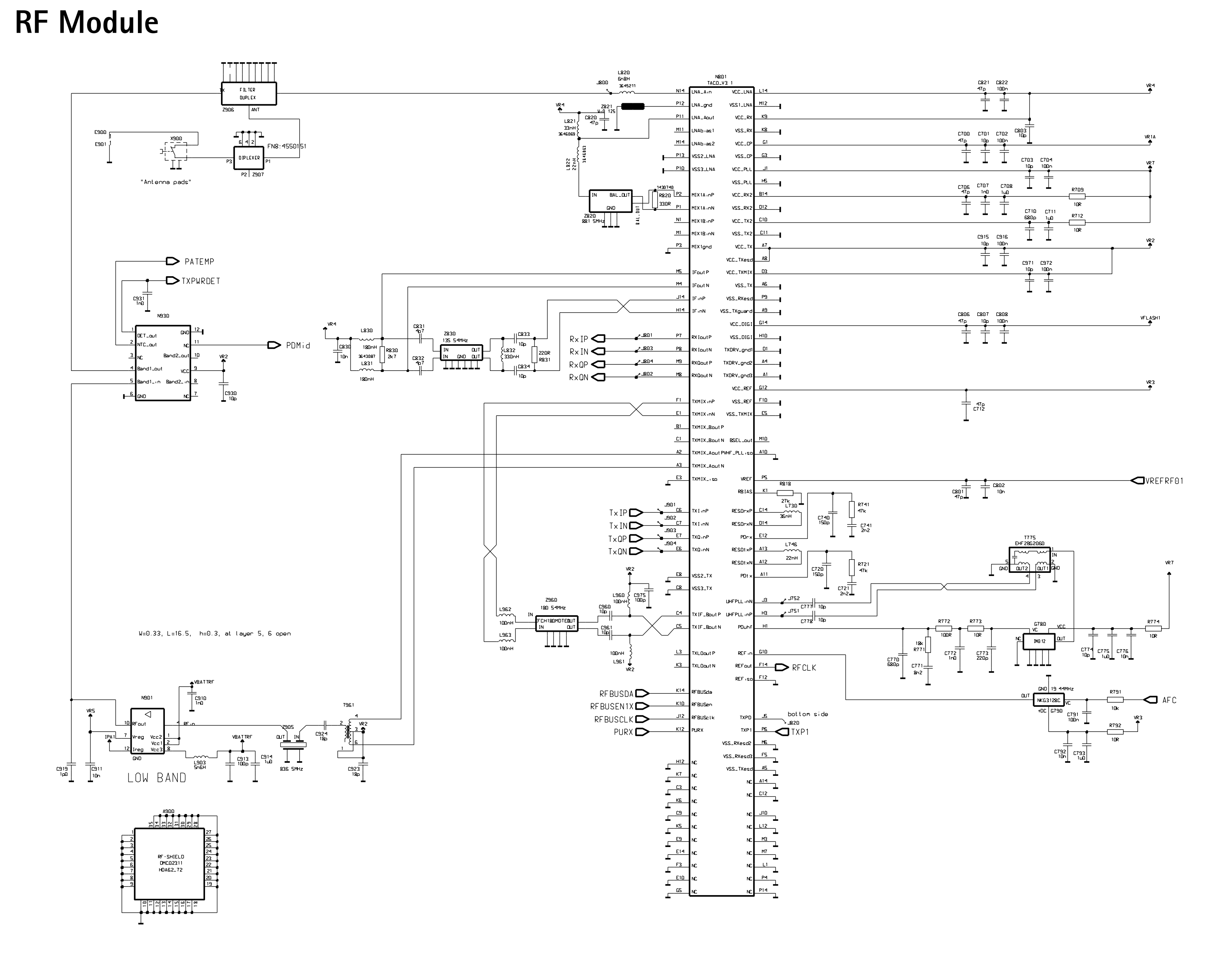




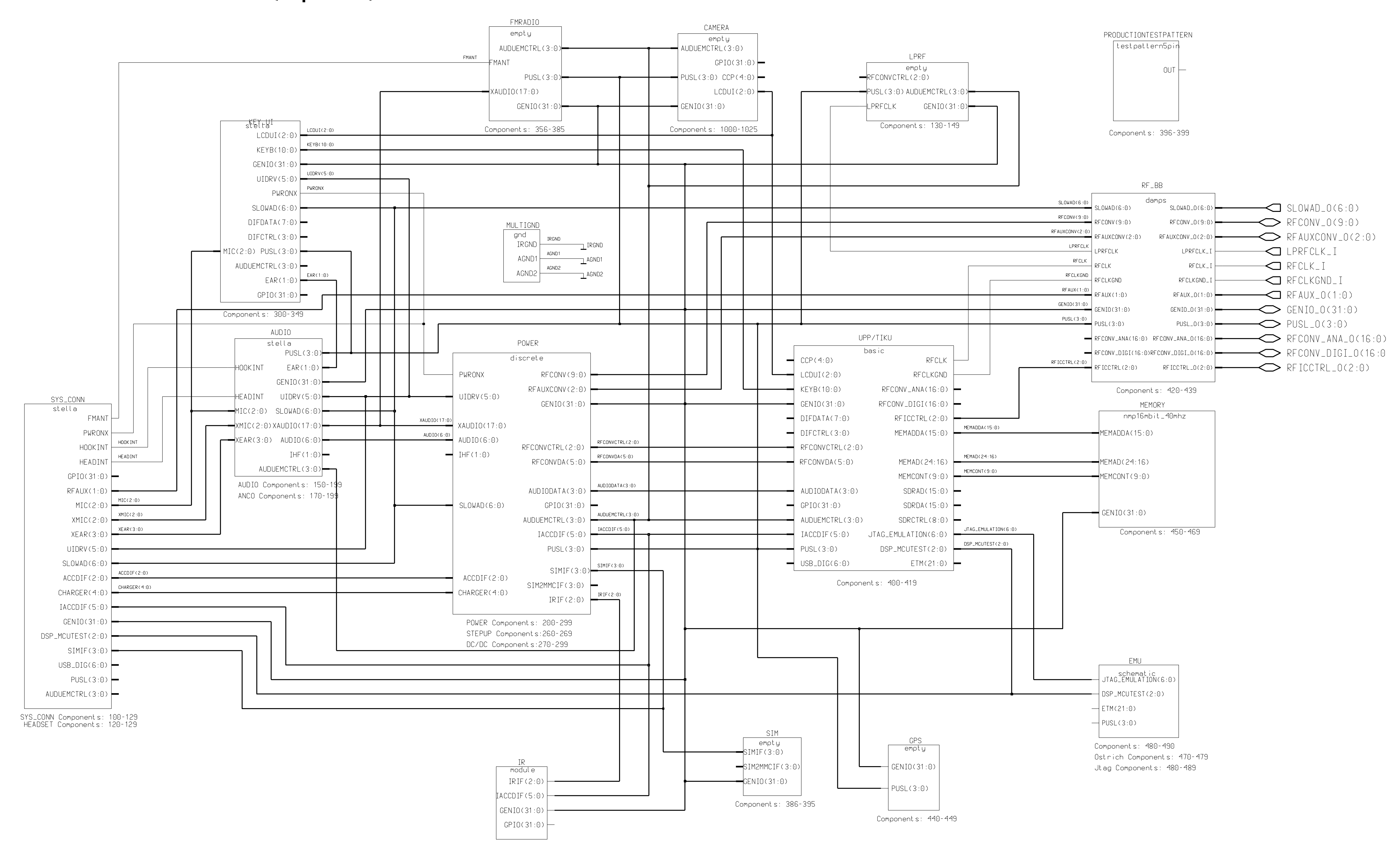
RF Block Diagram



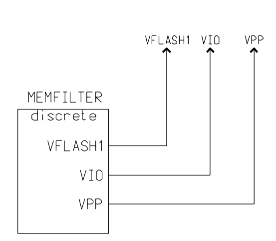
RF Module

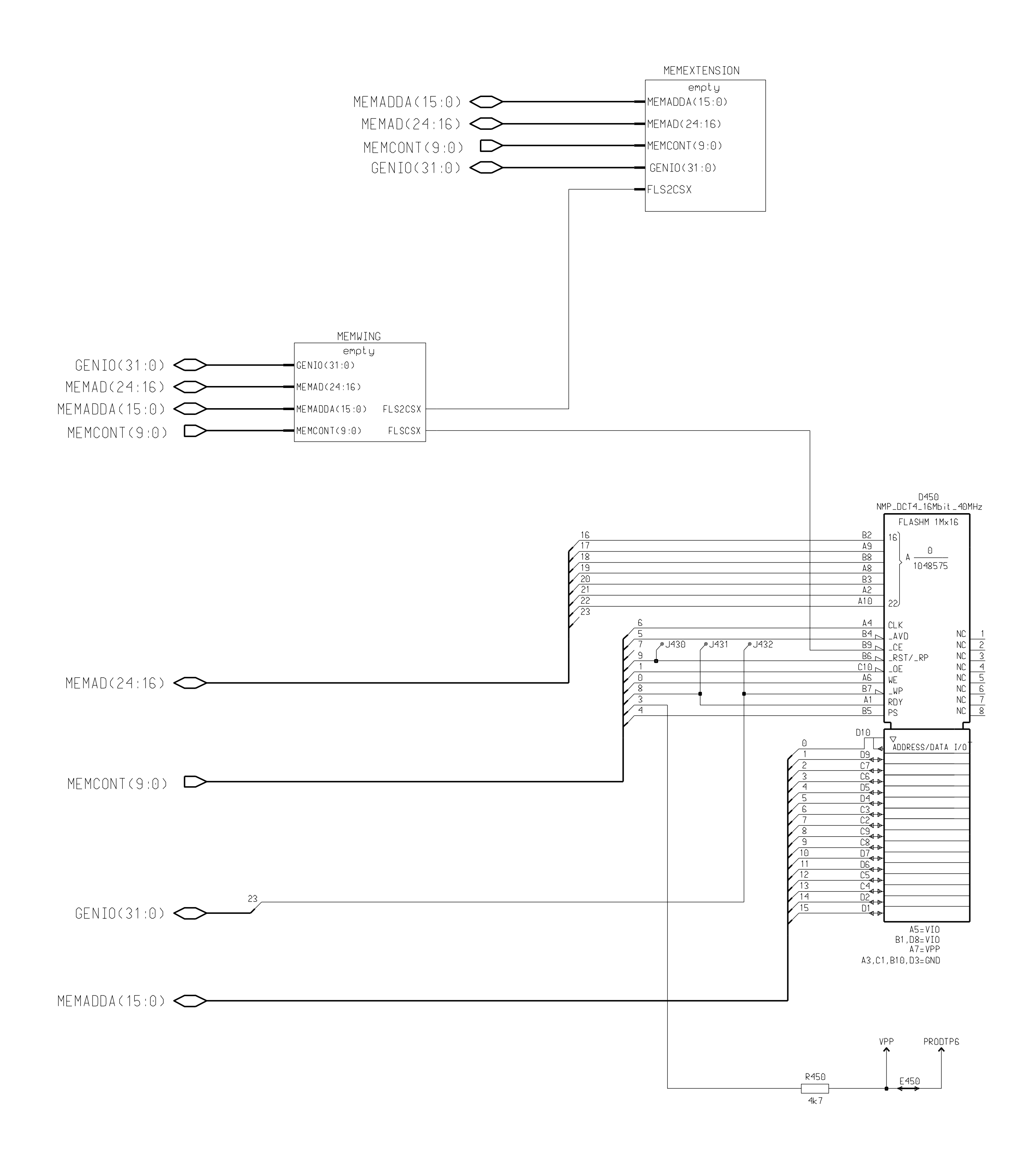

PATEMP
TXPWRDET
PDMid
RxIP
RxIN
RxQP
RxQN
TxIP
TxIN
TxQP
TxQN
VREFRF01
RFCLK
RFBUSDA
RFBUSEN1X
RFBUSCLK
PURX
TXP1
AFC
LOW BAND

DCT4 Common Baseband (Top-level)

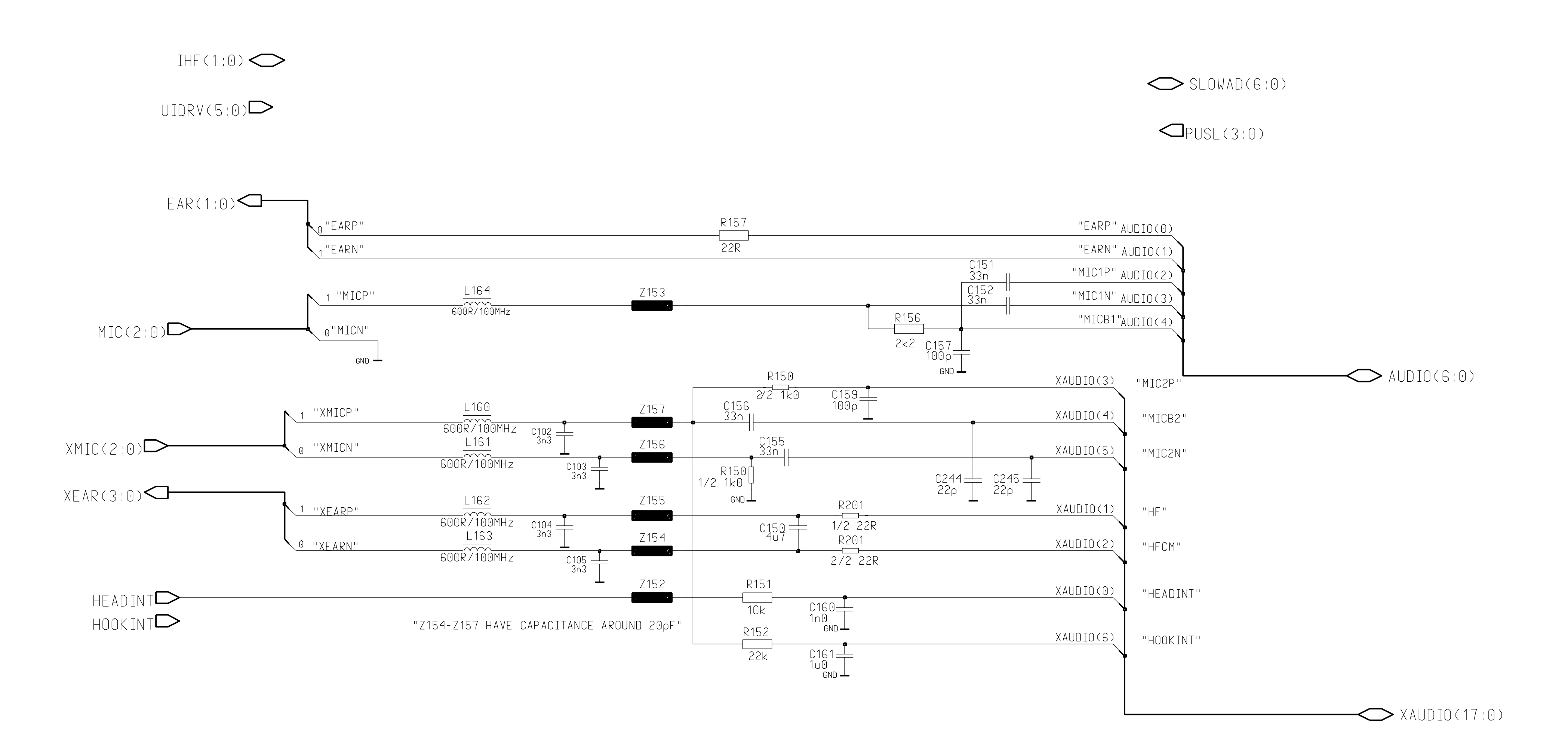


Flash Memory (32-bit)



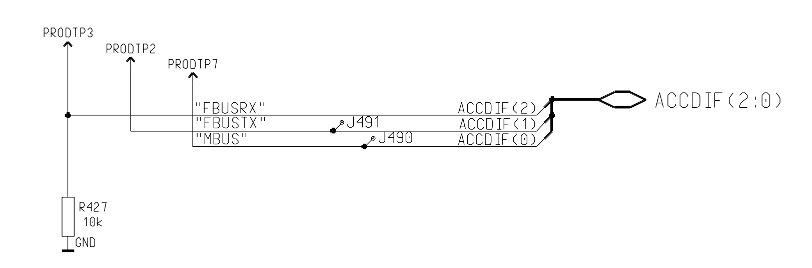


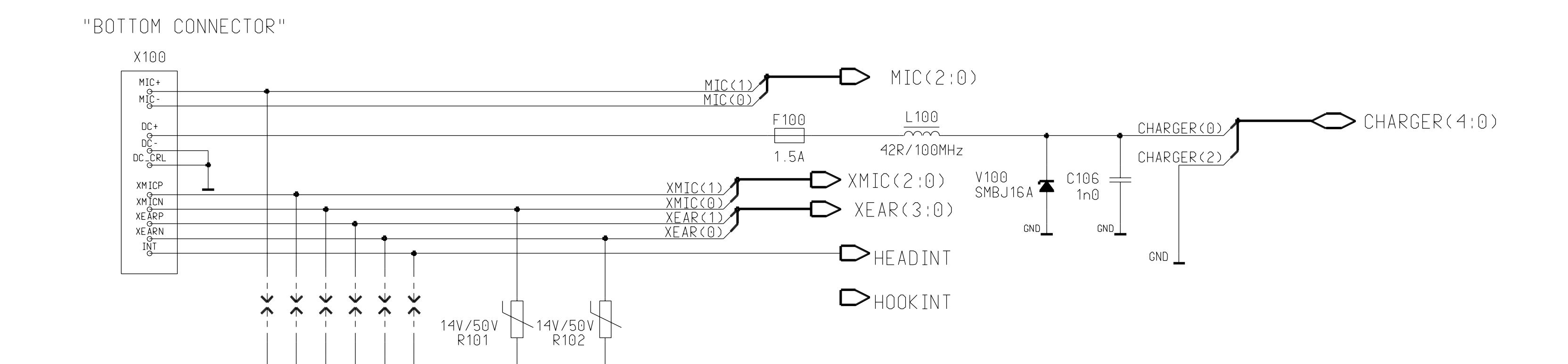
Sys/Audio

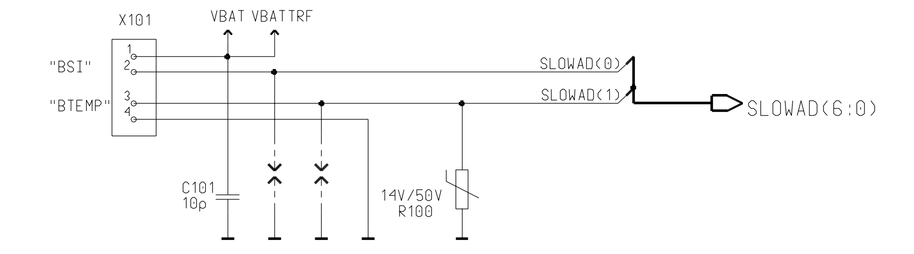

IHF(1:0)
UIDRV(5:0)
SLOWAD(6:0)
PUSL(3:0)
EAR(1:0)
MIC(2:0)
XMIC(2:0)
XEAR(3:0)
HEADINT
HOOKINT
AUDIO(6:0)
XAUDIO(17:0)
"Z154-Z157 HAVE CAPACITANCE AROUND 20pF"

Sys/System Connector

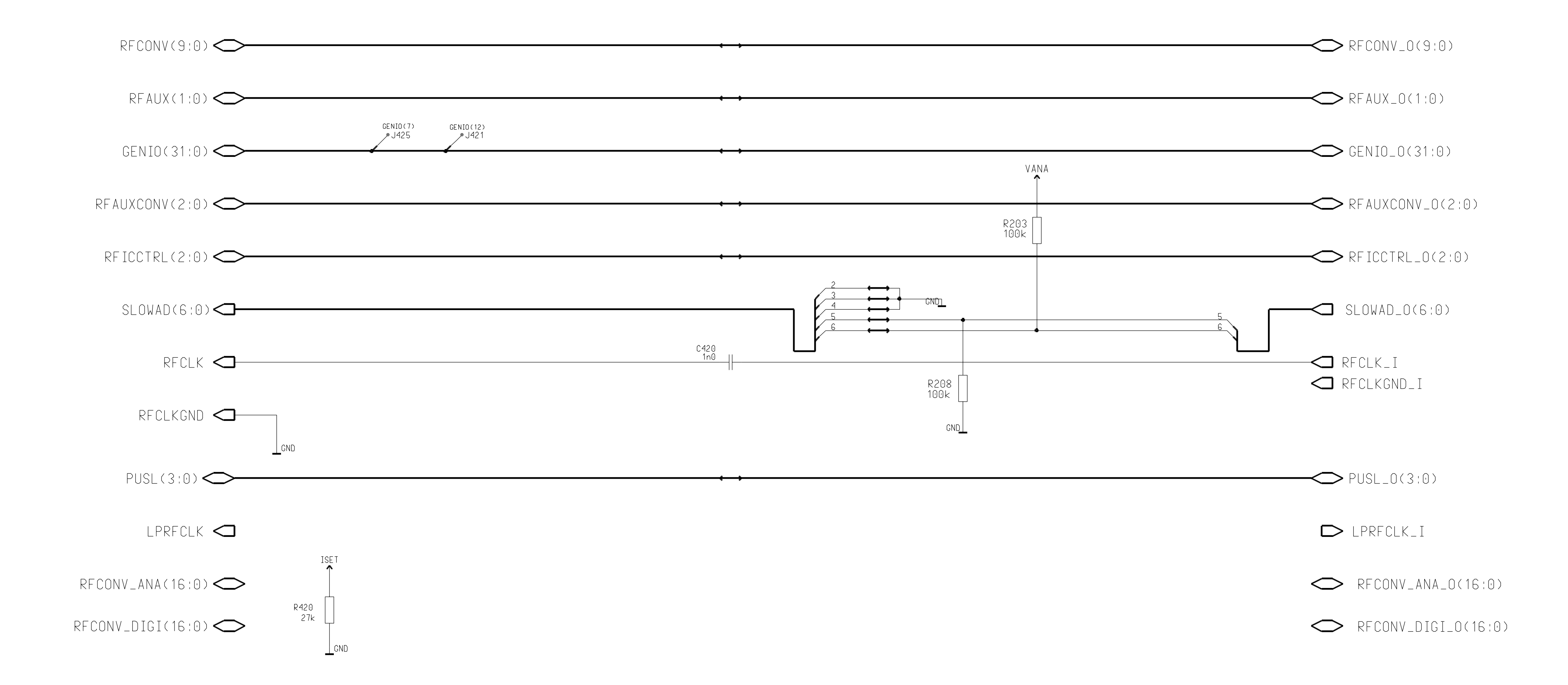
AUDUEMCTRL(3:0)
PUSL(3:0)
GENIO(31:0)
USB_DIG(6:0)
SIMIF(3:0)
DSP_MCUTEST(2:0)
IACCDIF(5:0)
RFAUX(1:0)
PWRONX
UIDRV(5:0)
FMANT
GPIO(31:0)



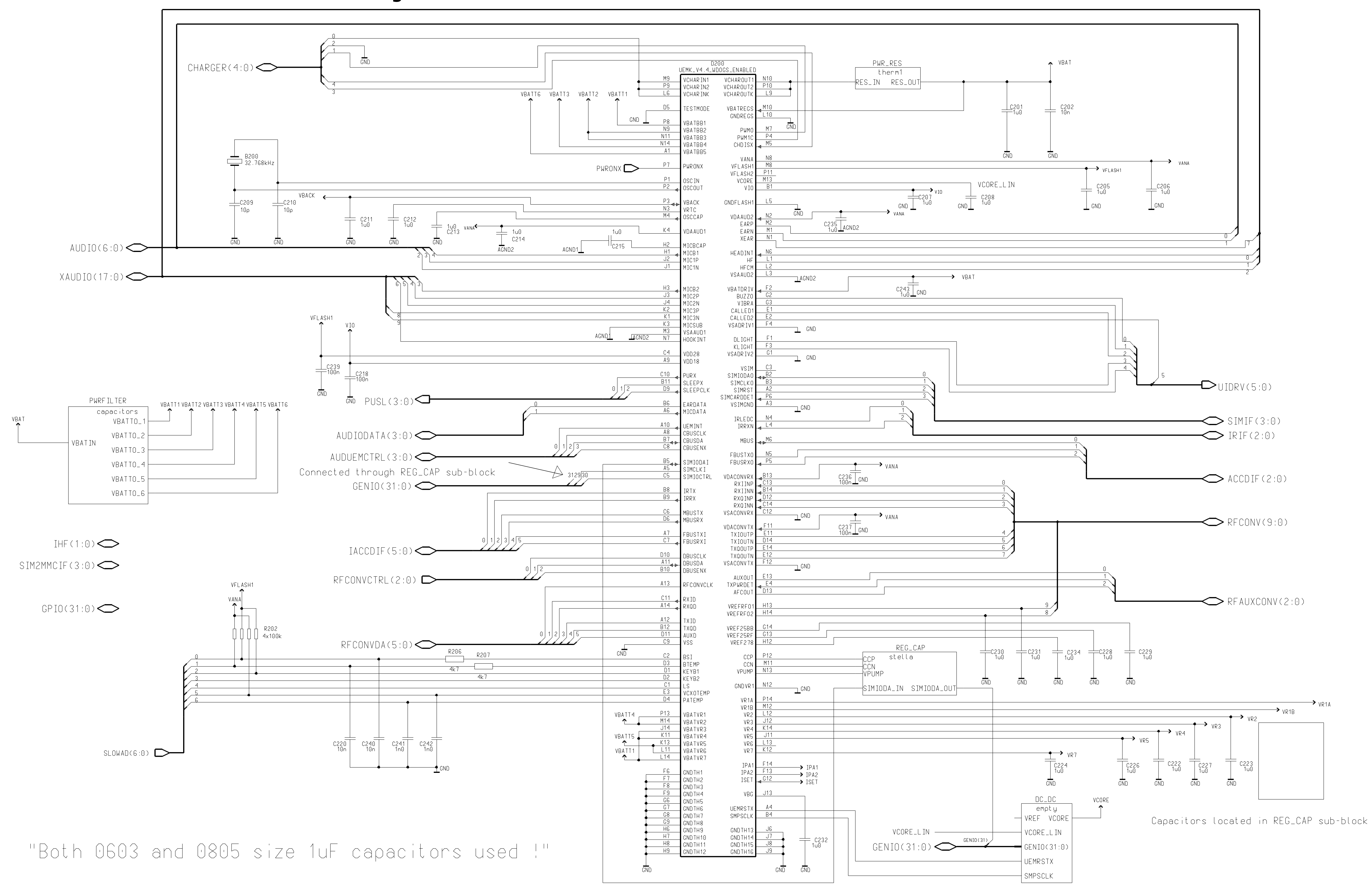




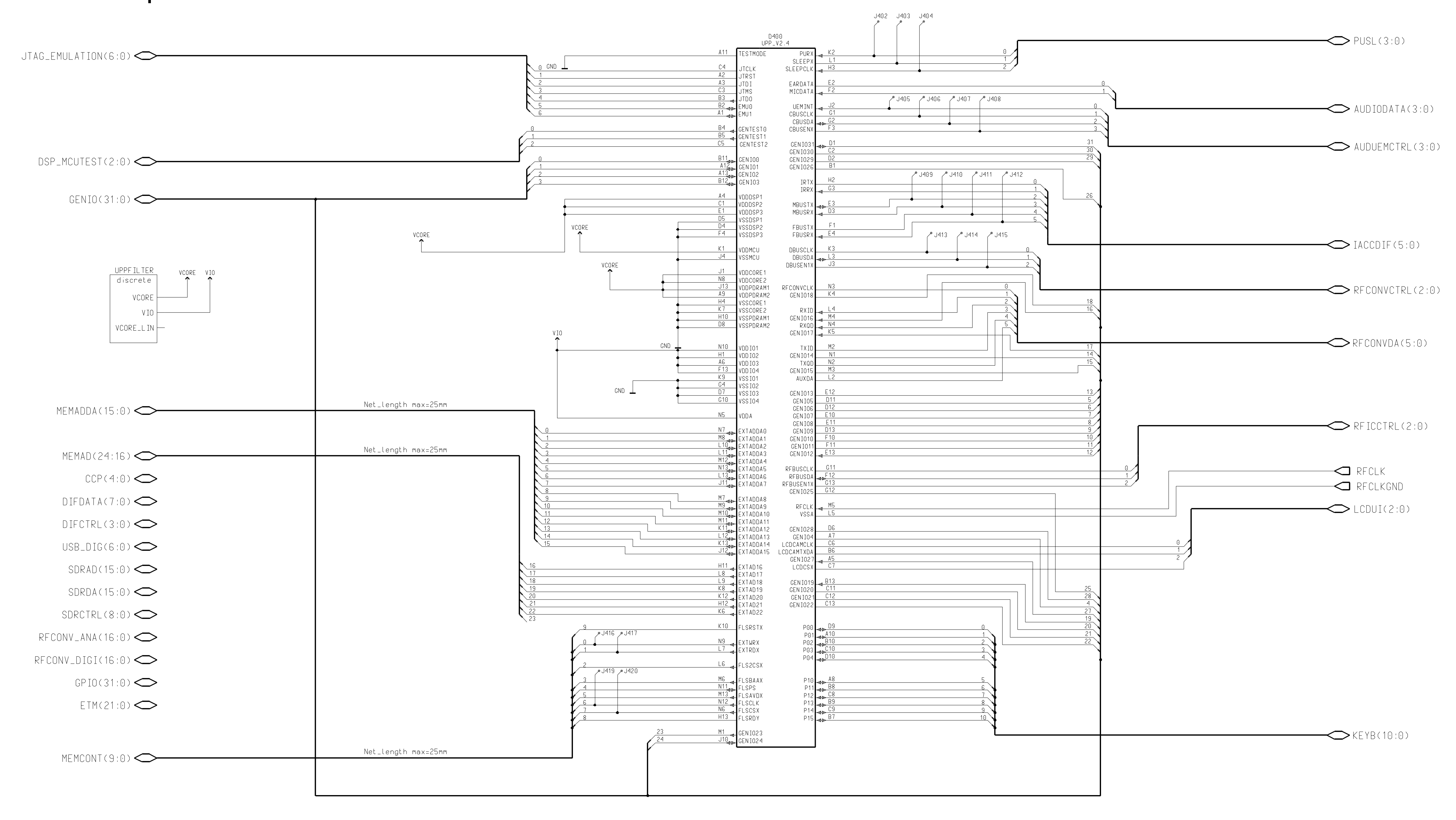
DAMPS RF - Baseband Interface



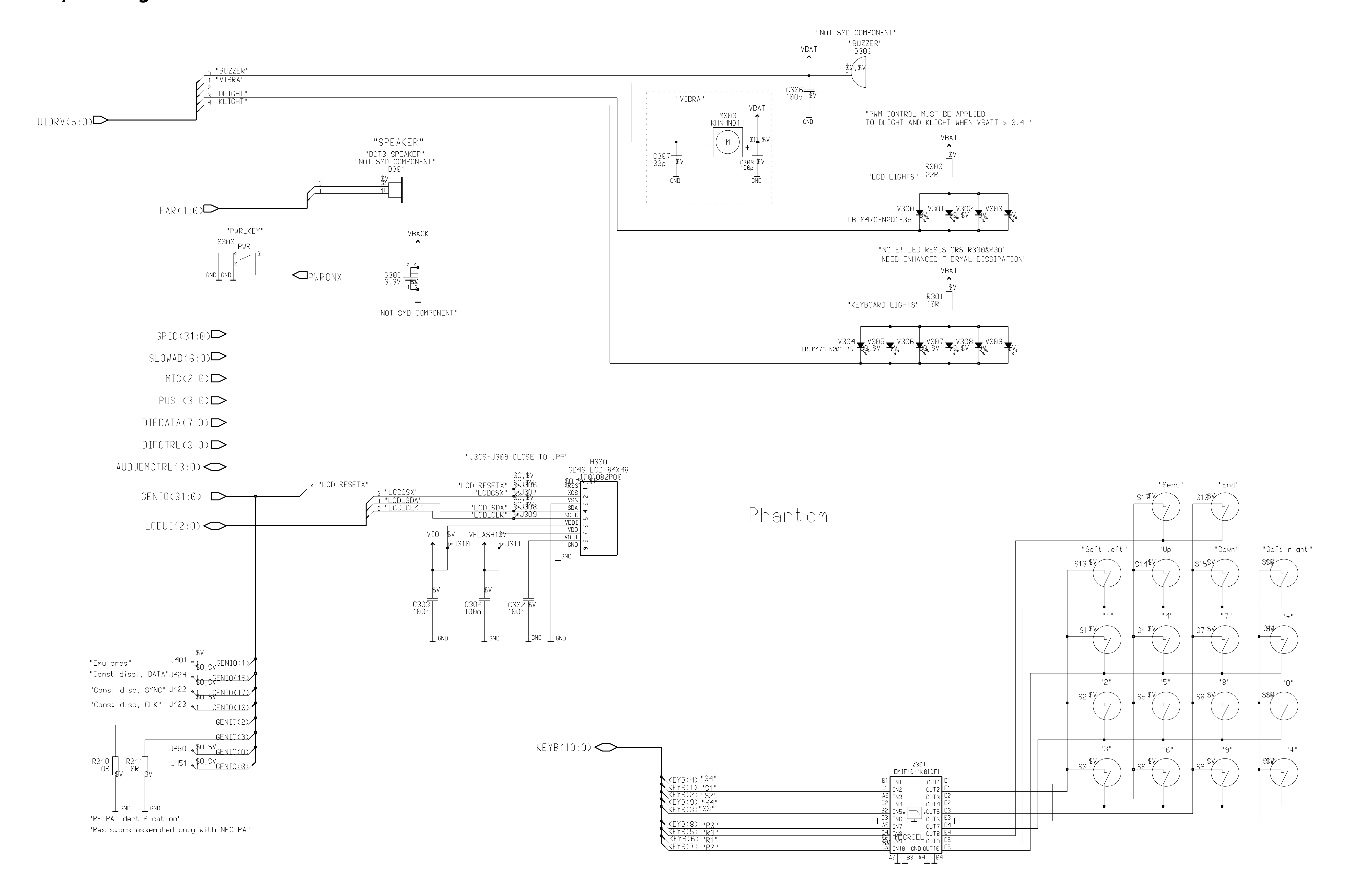
DCT4 Common BB - Discrete Power Management



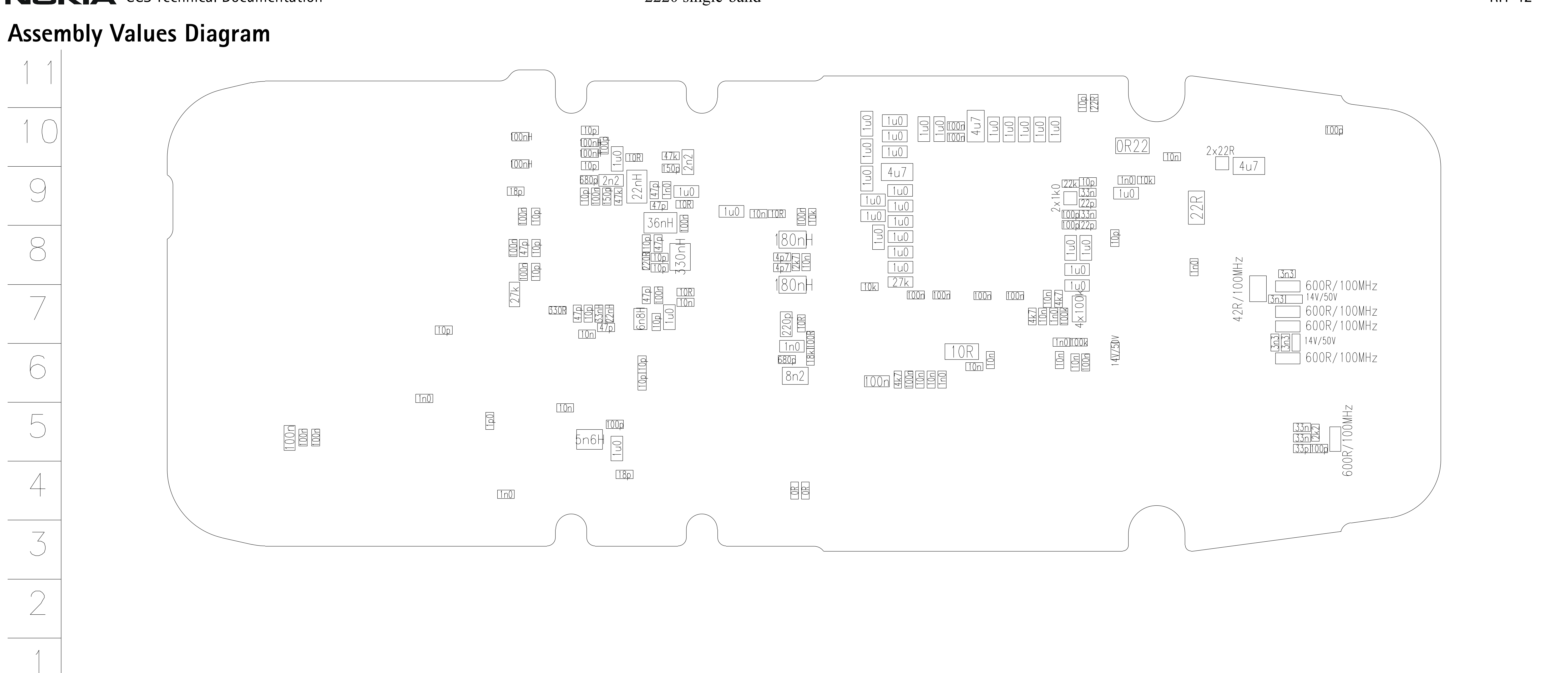
DCT4 UPP Implementation



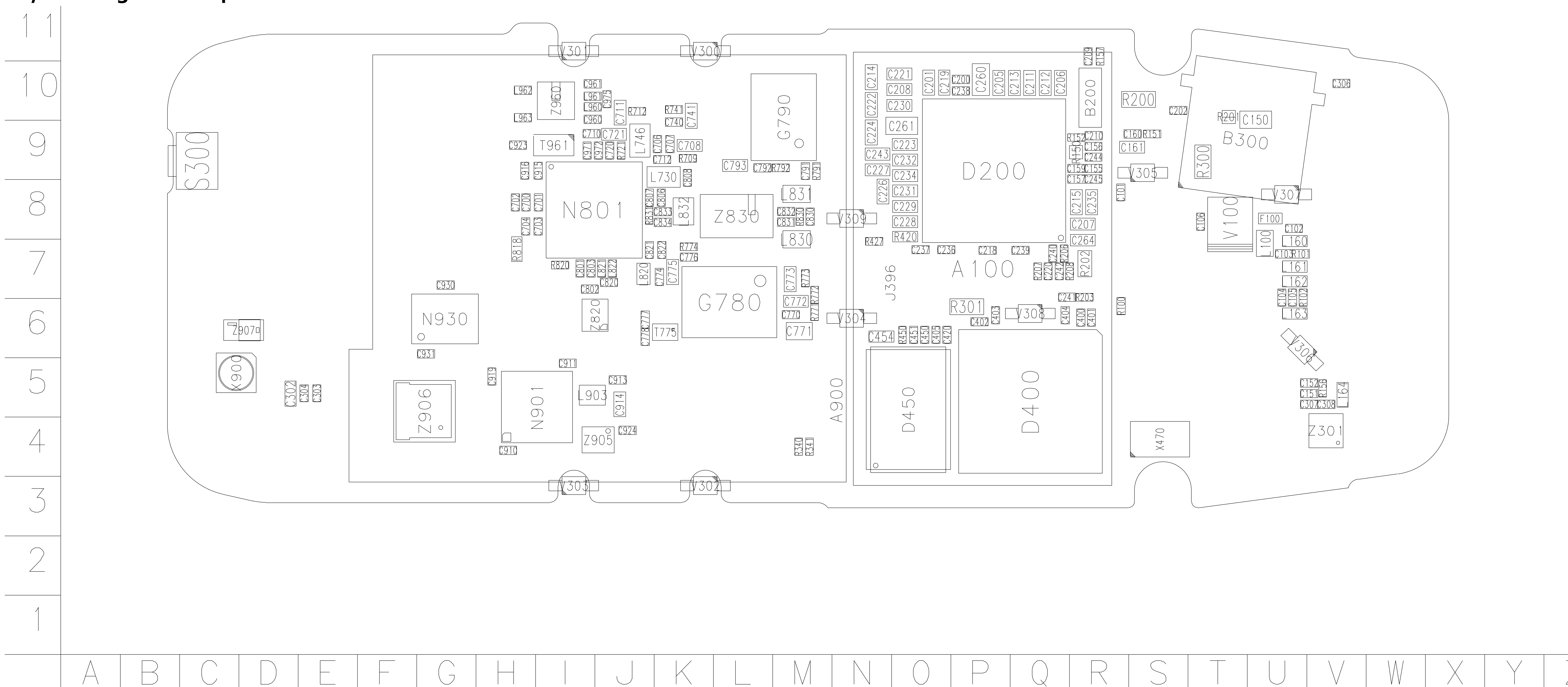
Sys - Key UI Diagram



Assembly Values Diagram



Layout Diagram - Top



Layout Diagram - Bottom

